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on March 29, 2004.

Julie Campbell  
Julie Campbell

In re application of:

**Mule, et al.**

Art Unit: **2874**

Serial No: **10/734,075**

Confirmation No.: 9140

Filed: **December 11, 2003**

Docket No.: **062004-1851**

For: **Backplane, Printed Wiring Board, and/or Multi-Chip Module-Level  
Optical interconnect Layer Having Embedded Air-Gap Technologies and  
Methods of Fabrication**

The following is a list of documents enclosed:

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Request for Correction of Filing Receipt  
Copy of Original Filing Receipt



**PATENTS**

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

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**Mule, et al.**

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**REQUEST FOR CORRECTION TO THE FILING RECEIPT**

Commissioner for Patents  
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P.O. Box 1450  
Alexandria, Virginia 22313-1450

Sir:

Applicant hereby advises the Commissioner that an error appears in the title of the invention.

Therefore, Applicant hereby requests that the word "Are-Gap" be changed to --Air-Gap--, and that a Corrected Filing Receipt be issued to the undersigned attorney.

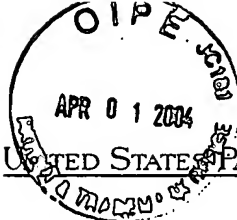
Respectfully submitted,

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100 Galleria Parkway, Suite 1750  
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Date: 3/29/04  
Customer No.: 24504  
Docket: **62004-1851**  
00054510



## UNITED STATES PATENT AND TRADEMARK OFFICE

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APPL NO.	FILING OR 371 (c) DATE	ART UNIT	FIL FEE REC'D	ATTY. DOCKET NO	DRAWINGS	TOT CLMS	IND CLMS
10/734,075	12/11/2003	2874	856	62004-1851	10	19	4

CONFIRMATION NO. 9140

24504  
THOMAS, KAYDEN, HORSTEMEYER & RISLEY, LLP  
100 GALLERIA PARKWAY, NW  
STE 1750  
ATLANTA, GA 30339-5948

## FILING RECEIPT



\*OC000000012143935\*

Date Mailed: 03/19/2004

Receipt is acknowledged of this regular Patent Application. It will be considered in its order and you will be notified as to the results of the examination. Be sure to provide the U.S. APPLICATION NUMBER, FILING DATE, NAME OF APPLICANT, and TITLE OF INVENTION when inquiring about this application. Fees transmitted by check or draft are subject to collection. Please verify the accuracy of the data presented on this receipt. If an error is noted on this Filing Receipt, please write to the Office of Initial Patent Examination's Filing Receipt Corrections, facsimile number 703-746-9195. Please provide a copy of this Filing Receipt with the changes noted thereon. If you received a "Notice to File Missing Parts" for this application, please submit any corrections to this Filing Receipt with your reply to the Notice. When the USPTO processes the reply to the Notice, the USPTO will generate another Filing Receipt incorporating the requested corrections (if appropriate).

## Applicant(s)

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## Domestic Priority data as claimed by applicant

This application is a CON of 10/135,314 04/29/2002 ABN  
which claims benefit of 60/287,440 04/30/2001

## Foreign Applications

If Required, Foreign Filing License Granted: 03/19/2004

Projected Publication Date: 07/01/2004

Non-Publication Request: No

THOMAS, KAYDEN  
HORSTEMEYER & RISLEY, L.L.P.

MAR 22 2004

DOCKETED

Early Publication Request: No

**Title**

Backplane, printed wiring board, and/or multi-chip module-level optical interconnect layer having embedded ~~are gap~~ technologies and methods of fabrication

**Preliminary Class**

385

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